IN THE UNITED STATES PATENT AND TRADEMARK OFFICE James Holt and John So

Fairchild Semiconductor Corporation

Alamana Applicants: Assignee: Title: Over-Voltage Protection Circuit Application No.: 10/027,708 Examiner: Filing Date: December 19, 2001 Urienown Docket No .: Group Art Unit: M-11811 US 2836 Confirmation No.: 4177 COMMISSIONER FOR PATENTS San Francisco, California Washington, D. C. 20231 February 19, 2003 NOTICE OF CHANGE OF ADDRESS Dear Sir: Please direct all correspondence in the above-identified application and with respect to any patent that issues on this application to the undersigned at this address: c/o SIDLEY AUSTIN BROWN & WOOD LLP 555 CALIFORNIA STREET SUITE 5000 TECHNOLOGY CENTER 2800 SAN FRANCISCO, CA 94104-1715 TEL. (415) 772-1200 MAR -3 FAX (415) 397-4621 Certificate of Mailing 2003 I hereby certifiy that this correspondence is being deposited with the United States Postal Service on the date shown below with Respectfully submitted, sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 2/19/08 Philip W. Woo Date Attorney of Record Richard A. Pask Reg. No. 39,880 SF1 1331738v1 -1-